	Type	L#	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	0	Pietig-Achim.in. and @pd>20030907	USPAT; US-PGPUB; EPO; JPO	2004/02/17 16:30			0
2	BRS	L2	1	Pietig-A.in. and @pd>20030907	DERWENT	2004/02/17 16:30			0
3	BRS	L3	368	(710/301.ccls. 711/115.ccls. 711/104.ccls. 235/380.ccls. 902/39.ccls. 902/26.ccls. 902/25.ccls. 235/451.ccls. 235/441.ccls. 235/440.ccls. 235/492.ccls.) and @pd>20030907	USPAT; US-PGPUB	2004/02/17 16:34			0
4	BRS	L4	30	((ic chip smart) adj1 card) same contact$1 same (contactless (contact adj less) (non adj contact) noncontact wireless rf radio electromagnetic$4) same (memory storage data program$3 function$3 application$1) same (access$3 authoriz$3 permit$4 enabl$3) and @pd>20030907	USPAT; US-PGPUB	2004/02/17 16:47			0
5	BRS	L5	8	((ic chip smart) adj1 card) same contact$1 same (contactless (contact adj less) (non adj contact) noncontact wireless rf radio electromagnetic$4) same (memory storage data program$3 function$3 application$1) same (access$3 authoriz$3 permit$4 enabl$3) and @pd>20030907	EPO; JPO; DERWENT; IBM_TDB	2004/02/17 17:20			0
6	BRS	L6	4	((smart chip ic) adj1 card) same ((issuer host) near5 updat$3) and @pd>20030907	USPAT; US-PGPUB	2004/02/17 17:26			0
7	BRS	L7	0	((smart chip ic) adj1 card) same ((issuer host) near5 updat$3) and @pd>20030907	EPO; JPO; DERWENT; IBM_TDB	2004/02/17 17:27			0

	Type	L#	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
8	BRS	L8	3	((smart chip ic circuit memory) adj card) near5 (single one) near5 (nonvolatile flash eprom eeprom) near5 (program$1 application$1) and @pd>20030907	USPAT; US-PGPUB	2004/02/17 17:28			0
9	BRS	L9	8	((smart chip ic circuit memory) adj card) near5 (nonvolatile flash eprom eeprom) near5 (program$1 application$1) near5 data and @pd>20030907	USPAT; US-PGPUB	2004/02/17 17:29			0
10	BRS	L10	1	((smart chip ic circuit memory) adj card) near5 (nonvolatile flash eprom eeprom) near5 (program$1 application$1) near5 data and @pd>20030907	EPO; JPO; DERWENT; IBM_TDB	2004/02/17 17:32			0
11	BRS	L11	2	((ic smart circuit) adj1 card$1) near5 ((plural multiple several many different) near3 (memor$3 chip$1) near5 ((one single) near5 (memor$3 chip$1))) and @pd>20030907	USPAT; US-PGPUB	2004/02/17 17:33			0
12	BRS	L12	0	((ic smart circuit) adj1 card$1) near5 ((plural multiple several many different) near3 (memor$3 chip$1) near5 ((one single) near5 (memor$3 chip$1))) and @pd>20030907	EPO; JPO; DERWENT; IBM_TDB	2004/02/17 17:34			0